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Inventor: Barrie Gilbert
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Title: SQUARING CELLS AND MULTIPLIERS USING SUMMED EXPONENTIALS

SUPPLEMENTAL INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)U.S. PATENT DOCUMENTS

Exam Init	Ref	Document Number	Issue Date	Name	Class	Sub- Class
<u>mm</u>	—	5,359,293	10/94	Boksiner, et al.	324	544
<u>h</u>	—	5,473,244	12/95	Libove, et al.	324	126
<u>mm</u>	—	6,204,719B1	03/01	Gilbert	327	349

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS

Exam Init	Ref	(Including Author, Title, Date, Pertinent Pages, Etc.)
<u>mm</u>	—	Analog Devices <i>High Precision, Wide-Band RMS-to-DC Converter (AD637) Rev. E</i> , 10 pages, 1999
<u>h</u>	—	Analog Devices data sheets entitled "Dual, Low Noise, Wideband Variable Gain Amplifiers", AD600/AD602, Rev. A, 20 pages, published April 1992
<u>h</u>	—	Analog Devices data sheets entitled "Low Level, True RMS-to-DC Converter", AD636, Rev. A, 8 pages, published September 1992
<u>mm</u>	—	Analog Devices data sheets for 60 MHz, 2000 V/ μ S Monolithic Op Amp AD844, 12 pages, 1990

Examiner: mmDate Considered: 5/5/05